

FIG.1  
Background Art

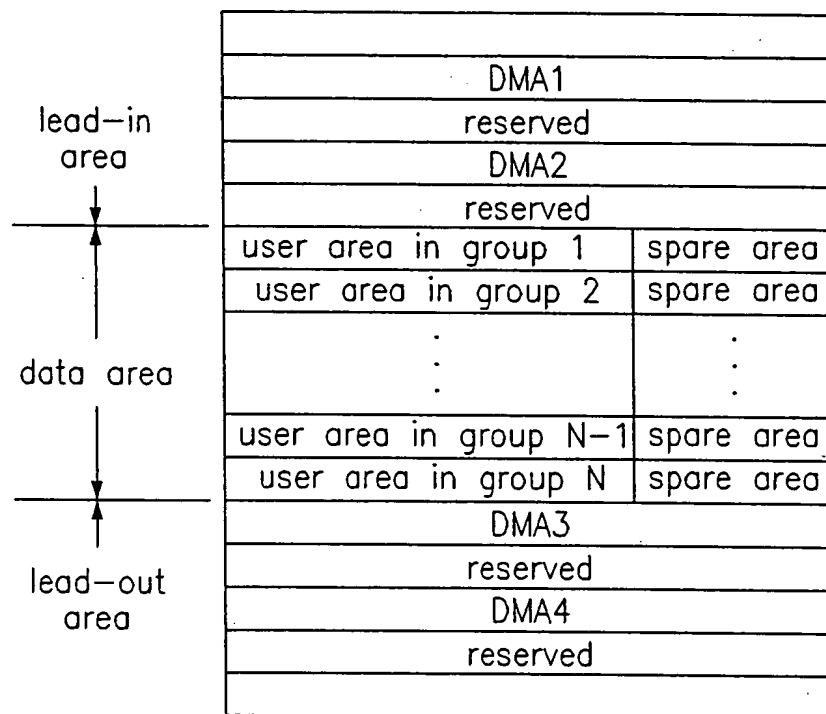


FIG.2A  
Background Art

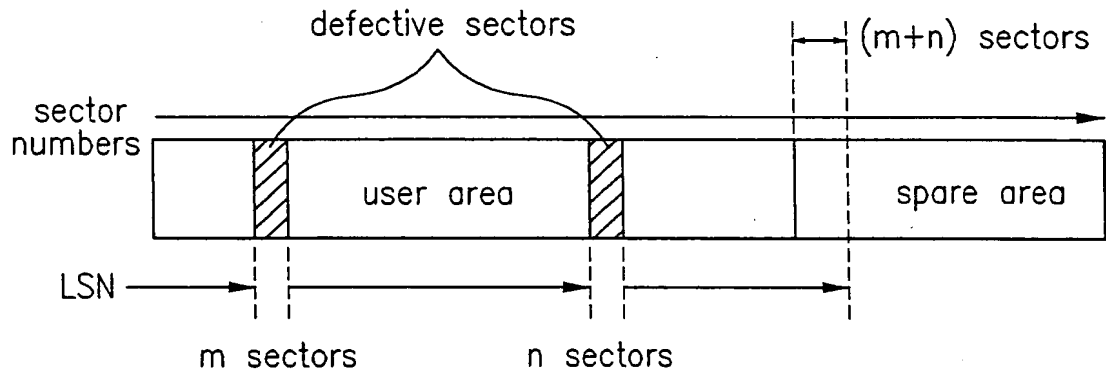


FIG.2B  
Background Art

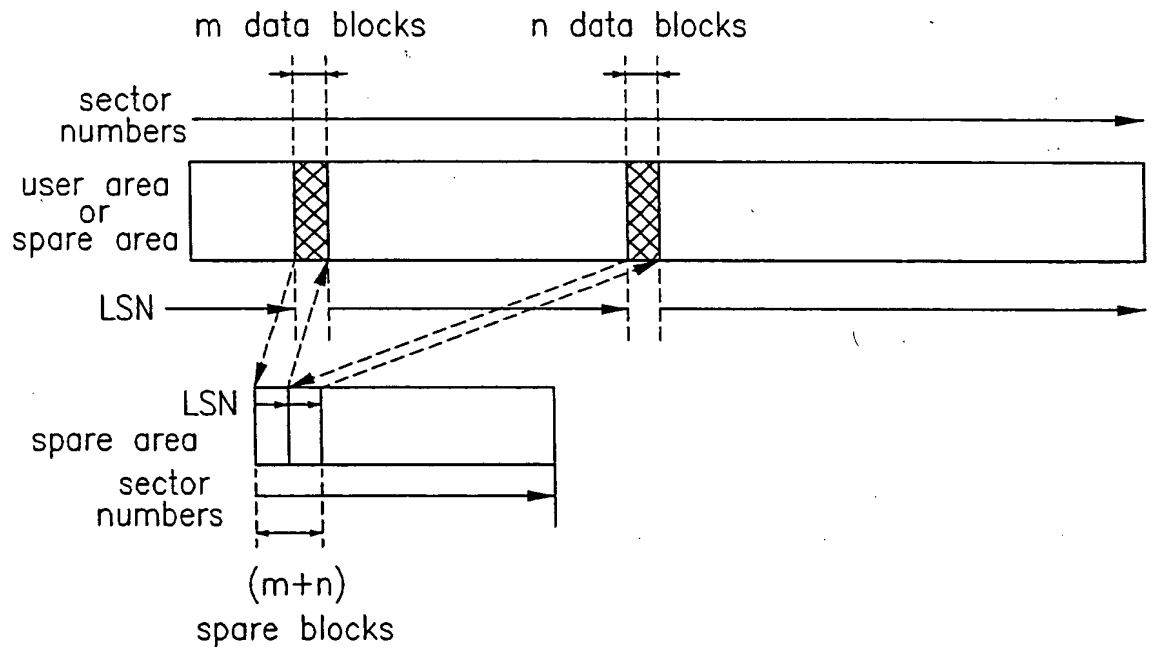


FIG.3  
Background Art

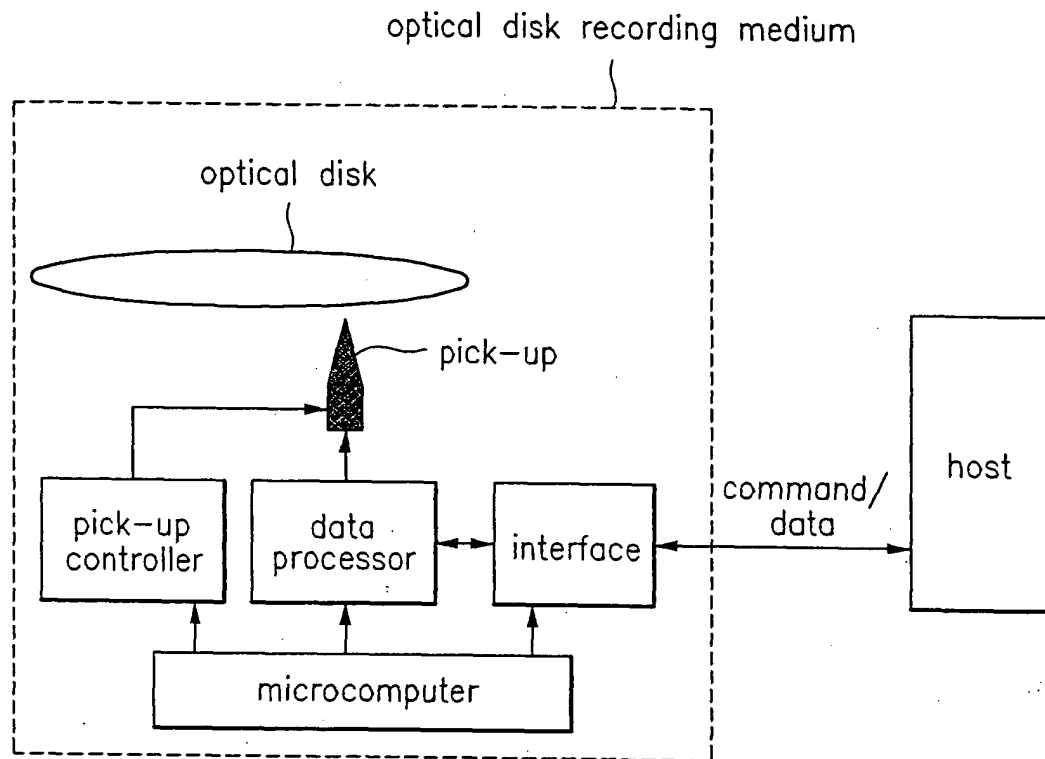


FIG.4A  
Background Art

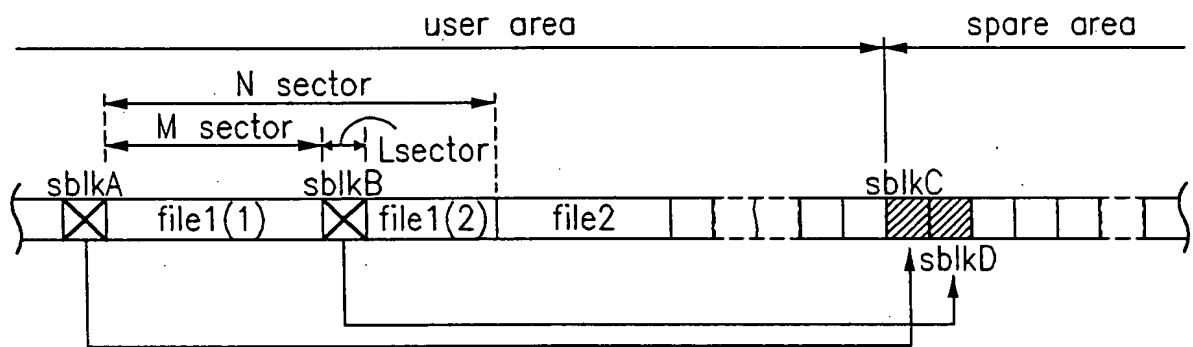


FIG.4B  
Background Art

	starting sector address	sector number
file1	A	N

FIG.4C  
Background Art

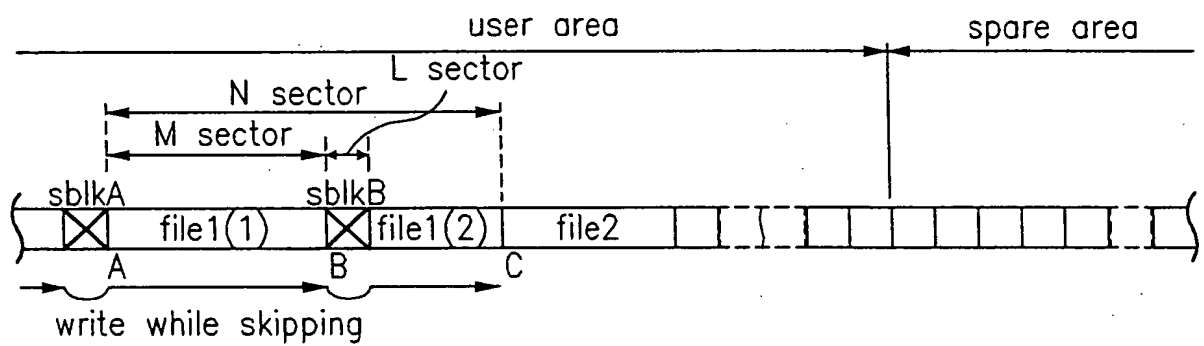


FIG.4D  
Background Art

	starting sector address	number of sectors
first representing method for file 1	A	N
second representing method for file 1	A	N-L

# FIG.5

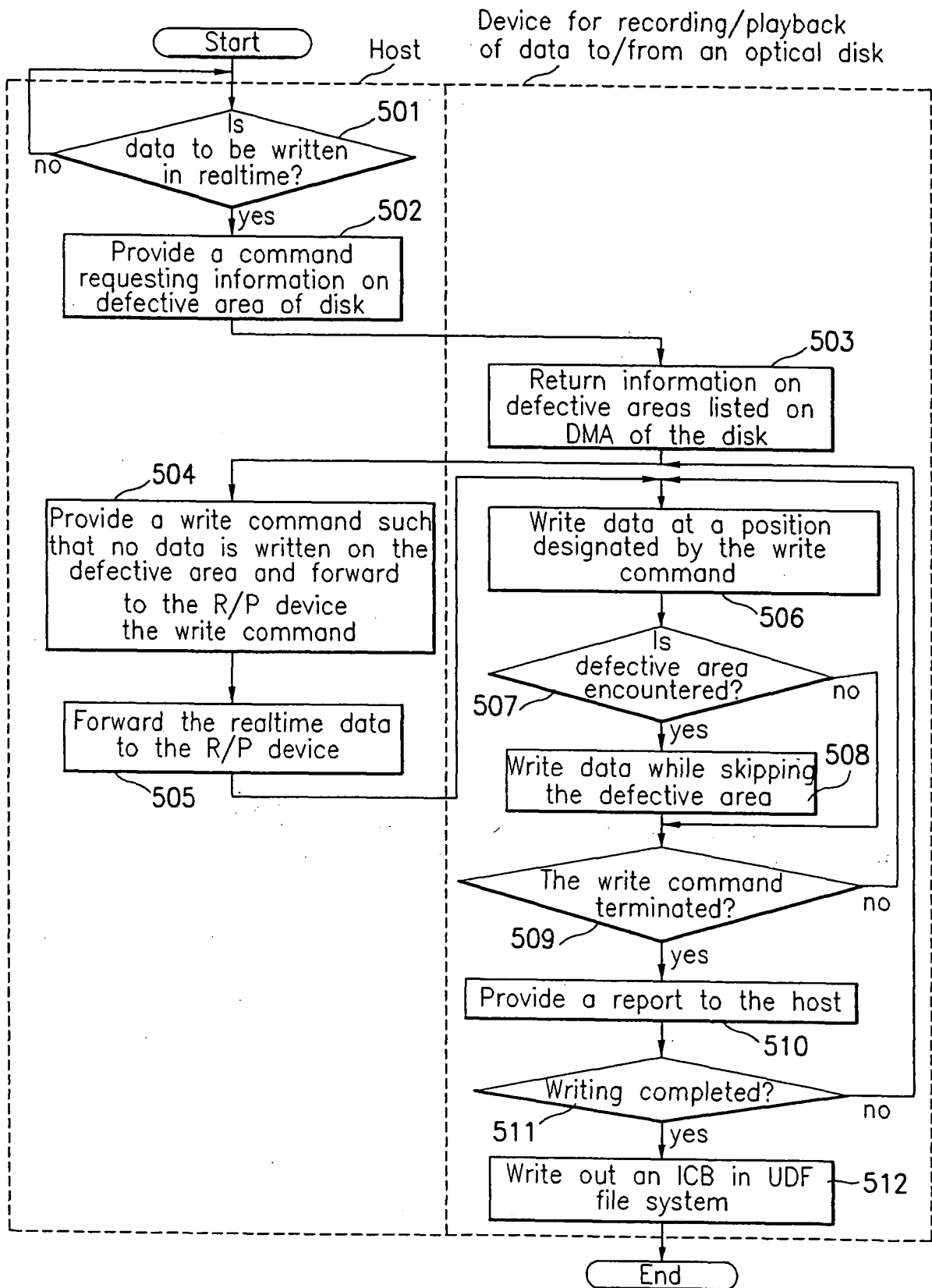
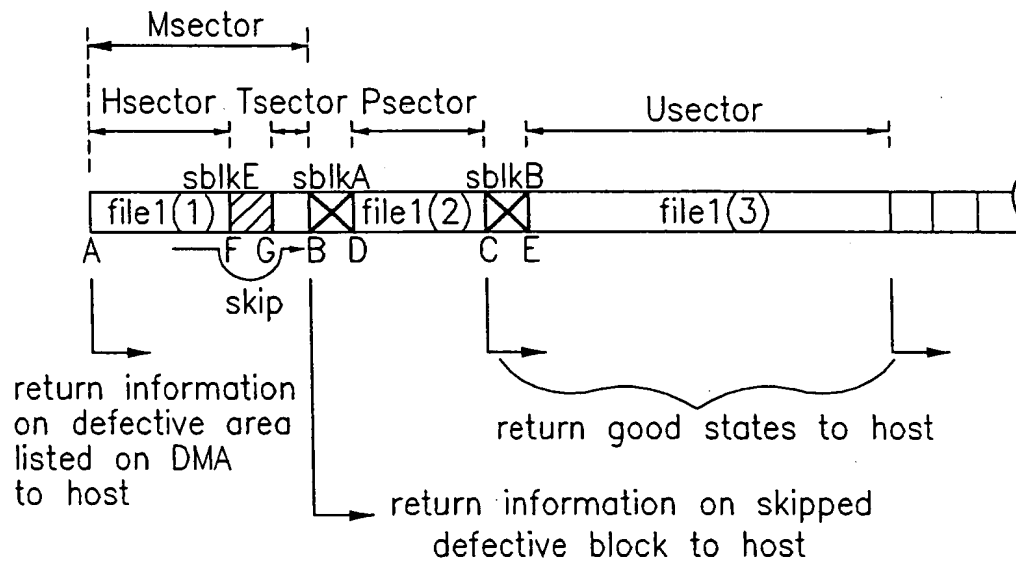


FIG.6A



▨ : defective block not listed on SDL

⊗ : defective block listed on SDL

FIG.6B

Byte	Bit	7	6	5	4	3	2	1	0
0									
1									
2				III	Reserved				
3									
4									
5									
6									
7									
8									
9									
10									
11									
12									
13									
14									
15									
16									
17									
18									
19									
20									

:

:

:



FIG.6C

	starting sector address	number of sectors
file 1	A	H
	G	T
	D	P
	E	U

FIG.7

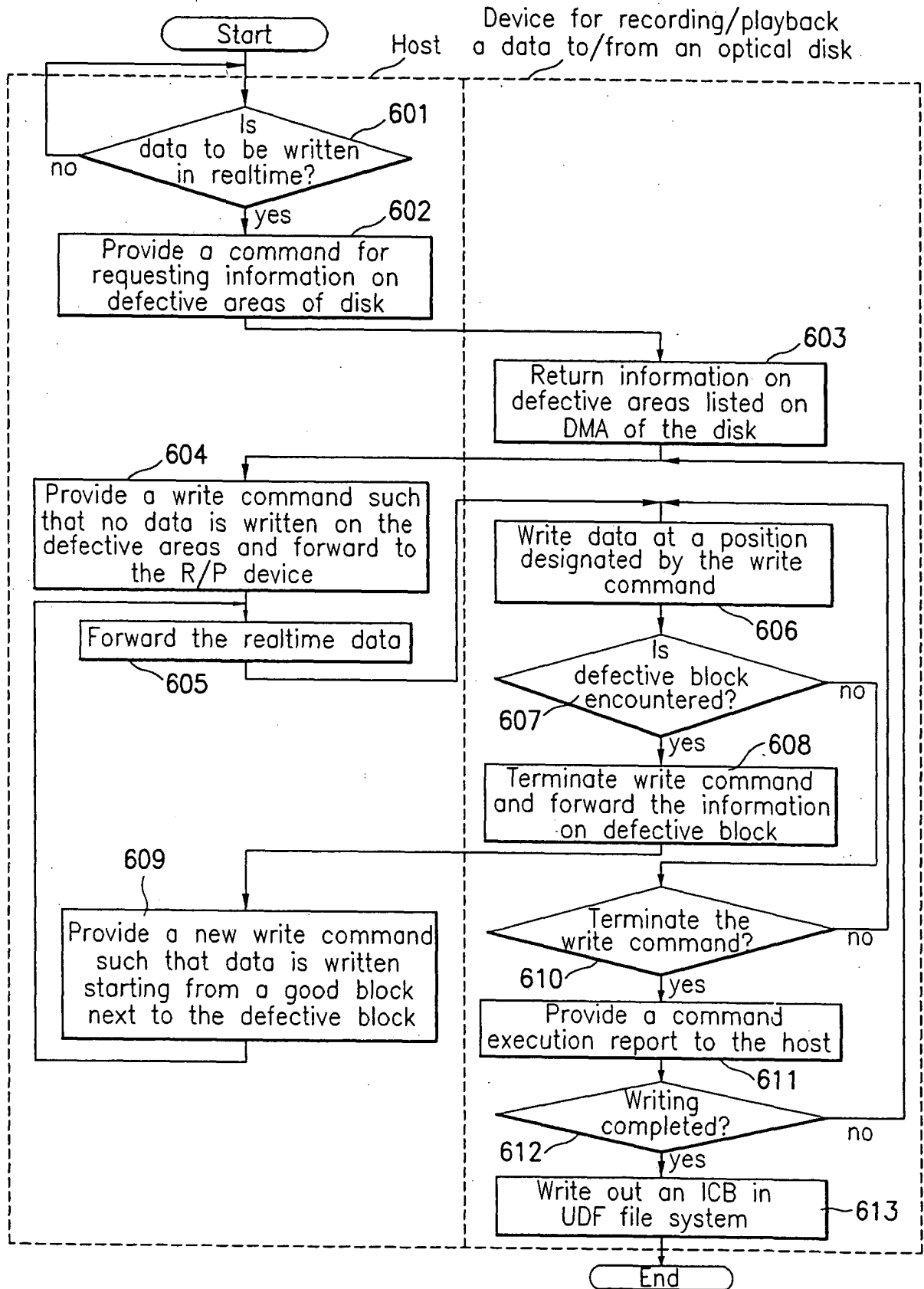


FIG.8A

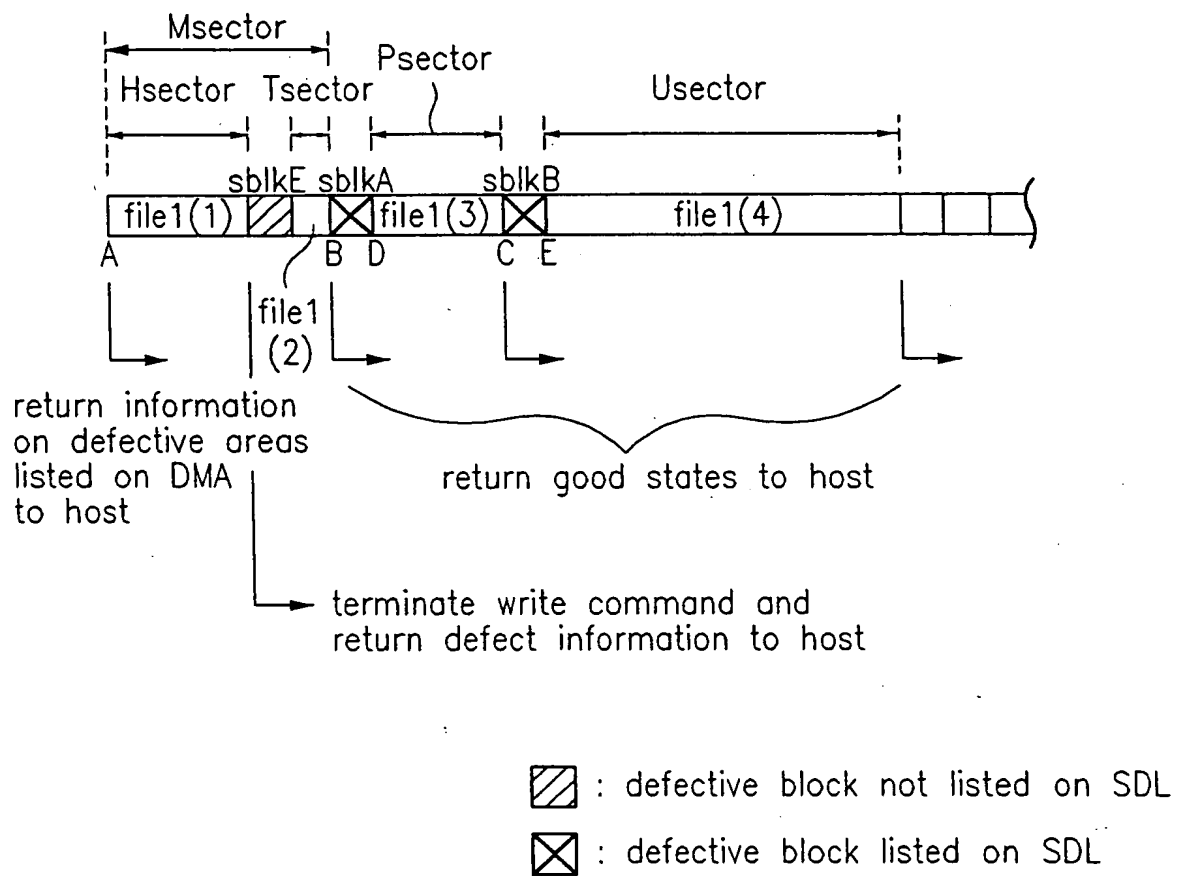


FIG. 8B

Byte	7	6	5	4	3	2	1	0
0						Error Code(70h)		
1	Reserved							
2			III	Reserved	Sense Key			
3	Information							
4								
5								
6								
7	Additional Sense Length							
8	listed sector number							
9								
10	consecutive defective sector number							
11								
12	Additional Sense Code							
13	Additional Sense Code Qualifier							
14	Field Replaceable Unit Code							

FIG.9

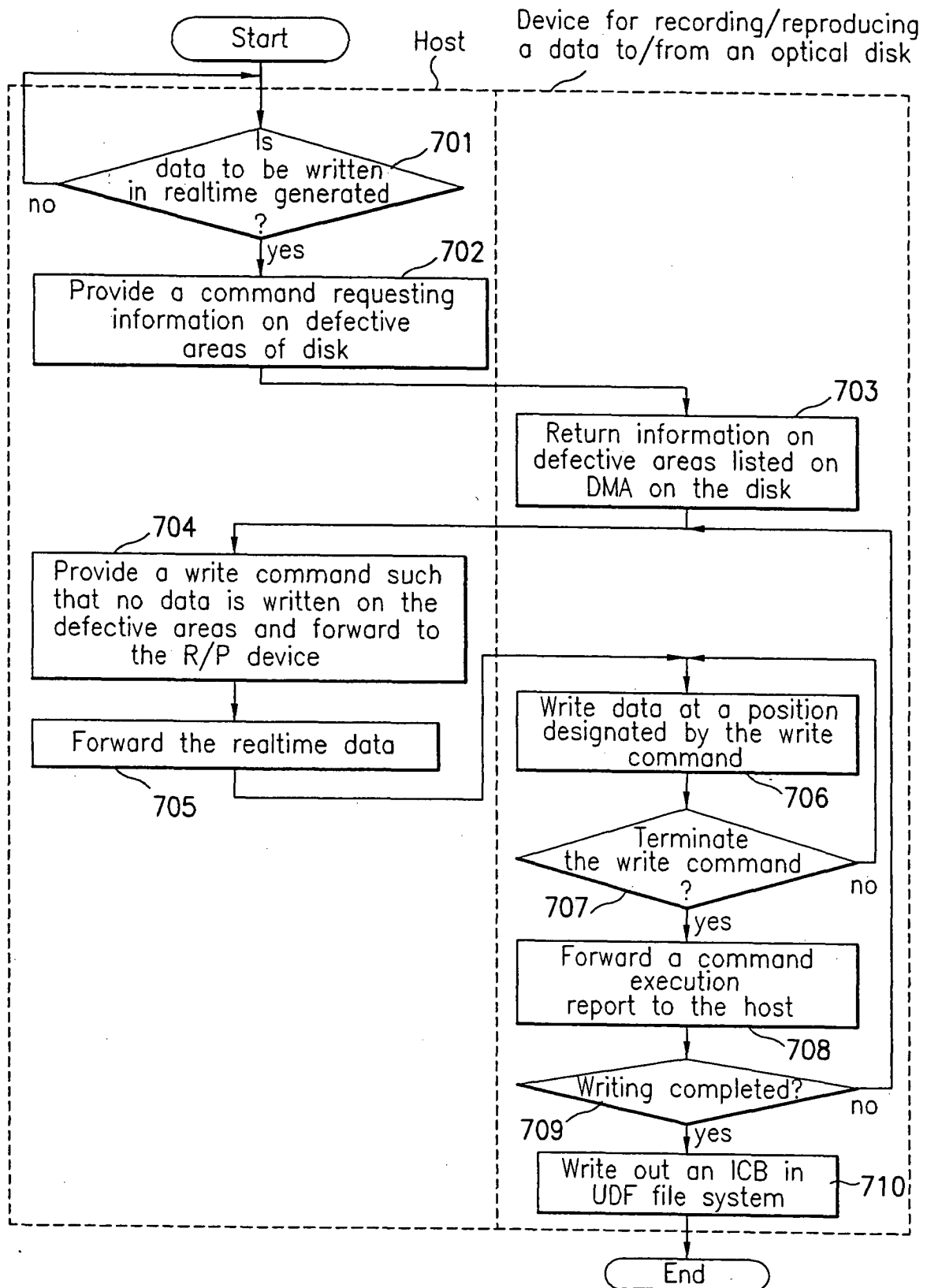
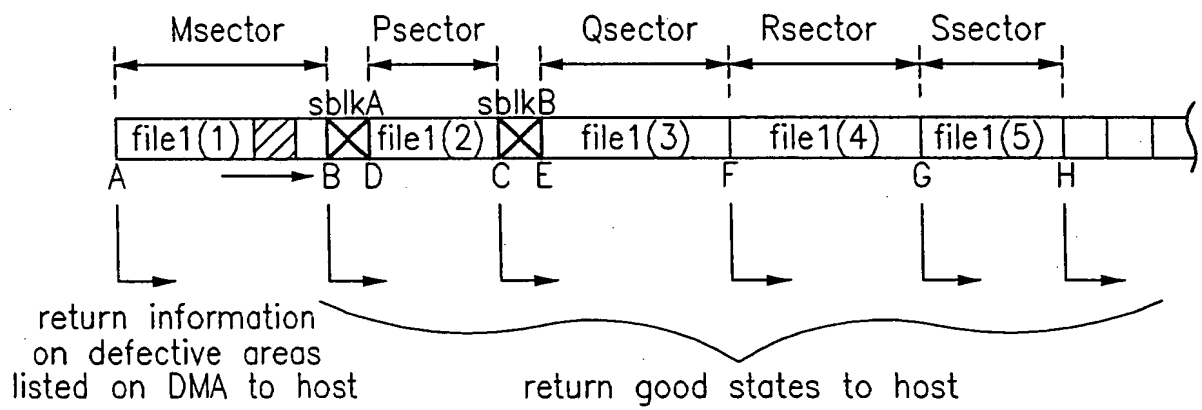


FIG.10A



▨ : defective blocks not listed on SDL

⊠ : defective blocks listed on SDL

FIG.10B

	starting sector address	sector number
file 1	A	M
	D	P
	E	Q + R + S

FIG. 11

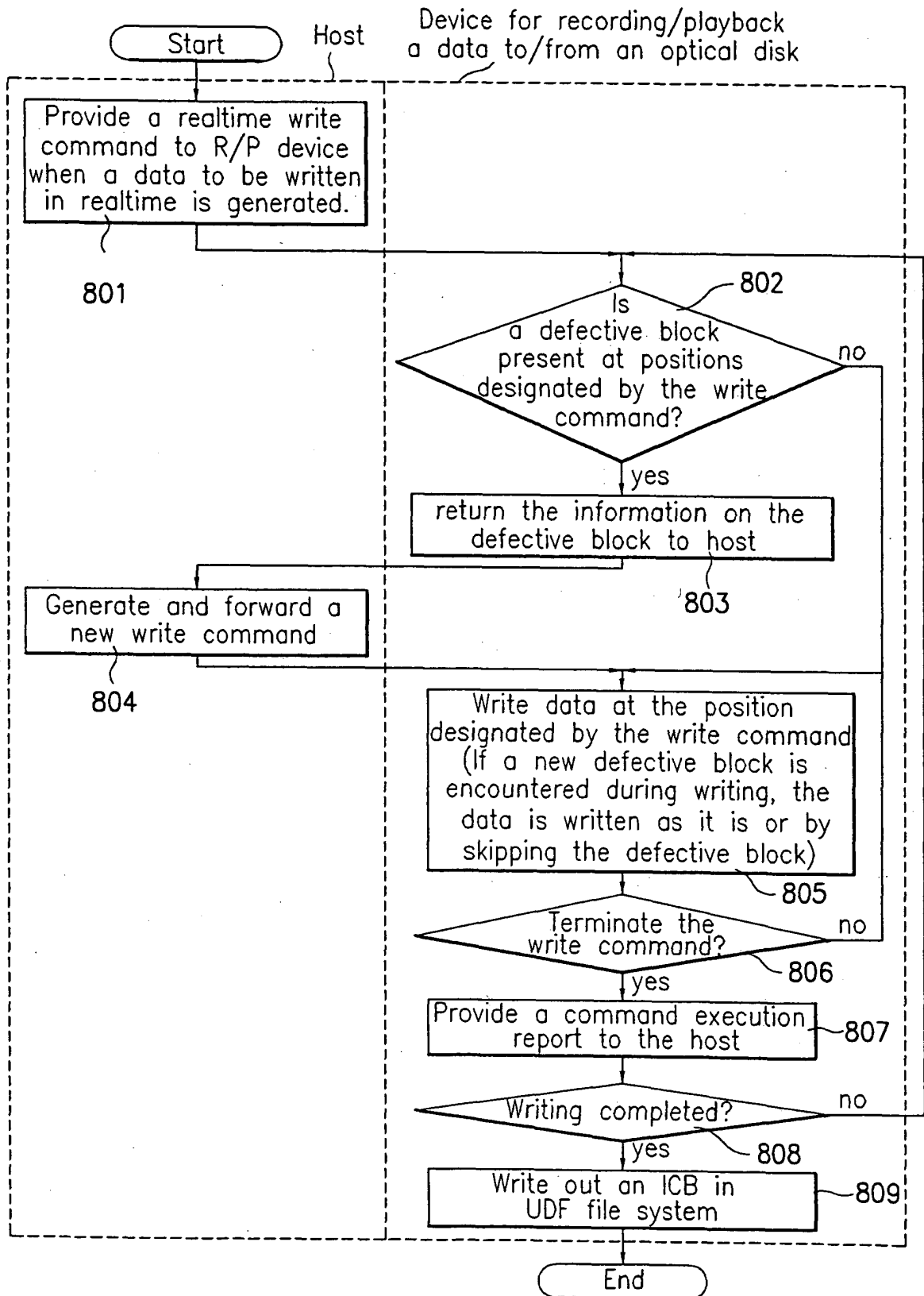




FIG.12

